A Rewriting System for the Vectorization of Signal Transforms

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Abstract. We present a rewriting system that automatically vectorizes signal transform algorithms at a high level of abstraction. The input to the system is a transform algorithm given as a formula in the well-known Kronecker product formalism. The output is a "vectorized" formula, which means it consists exclusively of constructs that can be directly mapped into short vector code. This approach obviates compiler vectorization, which is known to be limited in this domain. We included the formula vectorization into the Spiral program generator for signal transforms, which enables us to generate vectorized code and optimize through search over alternative algorithms. Benchmarks for the discrete Fourier transform (DFT) show that our generated floating-point code is competitive with and that our fixed-point code clearly outperforms the best available libraries.

1 Introduction

Most recent architectures feature short vector SIMD instructions that provide data types and instructions for the parallel execution of scalar operations in short vectors of length ν (called ν -way). For example Intel's SSE family provides $\nu = 2$ for double precision and $\nu = 4$ for single precision floating point arithmetic as well as $\nu = 8$ for 16-bit and $\nu = 16$ for 8-bit integer arithmetic. The potential speed-up offered by these instructions makes them attractive in domains were high performance is crucial, but they come at a price: Compilers often cannot make optimal use of vector instructions, since the necessary program transformations are not well understood; this moves the burden to the programmer, who is required to leave the standard C programming model, for example by using so-called intrinsics interfaces to the instruction set.

In [1] we have argued that for the specific domain of signal transforms such as the discrete Fourier transform (DFT) there is an attractive solution to this

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problem, namely to perform the vectorization at a higher level of abstraction by manipulating Kronecker product expressions through mathematical identities. The Kronecker product formalism has been known to be useful for the representation and derivation for DFT algorithms [2] but also in the derivation of parallel algorithms [3].

In this paper we describe an implementation of this formal vectorization in the form of a rewriting system [4], the common tool used in symbolic computation. We then include the rewriting system into the Spiral program generator [5], which uses the Kronecker product formalism as internal algorithm representations. This enables us to automatically generate optimized vectorized code through the search over available alternatives provided by Spiral. We show that our approach works for the DFT (speed-up in parenthesis) for 2-way (1.5 times) and 4-way floating point (3 times), and 8-way (5 times) and 16-way (6 times) integer code. Benchmarks of our generated code against the Intel MKL and IPP libraries and FFTW [6] show that our generated floating-point code is competitive and our generated fixed-point code is at least a factor of 2 faster than the vendor library.

Related work. The Intel C++ compiler includes a vectorizer based on loop vectorization and translation of complex operations into two-way vector code. FFTW 3.0.1 combines loop vectorization and an approach based on the linearity of the DFT to obtain 4-way single-precision SSE code [6]. This combines a hardcoded vectorization approach with FFTW's capability to automatically tune for the memory hierarchy. An approach to designing embedded processors with vector SIMD instructions and for designing software for these processors is presented in [7].

Organization of the paper. In Section 2 we provide background on SIMD vector instructions, signal transforms and their fast algorithms, and the Spiral program generator. The rewriting system is explained in Section 3 including examples of vectorization rules and vectorized formulas. Section 4 shows a number of runtime benchmarks for the DFT. We offer conclusions in Section 5

2 Background

SIMD vector instructions. Recently, major vendors of general purpose microprocessors have included short vector SIMD (single instruction, multiple data) extensions into their instruction set architecture. Examples of SIMD extensions include Intel's MMX and SSE family, AMD's 3DNow! family, and Motorola's AltiVec extension. SIMD extensions have the potential to speed up implementations in areas where the relevant algorithms exhibit fine grain parallelism but are a major challenge to software developers.

In this paper we denote the vector length with ν . For example, SSE2 provides 2-way ($\nu = 2$) double and 4-way ($\nu = 4$) single precision floating point as well as 8-way ($\nu = 8$) 16-bit and 16-way ($\nu = 16$) 8-bit integer vector instructions.

Signal transforms. A (linear) signal transform is a matrix-vector multiplication $x \mapsto y = Mx$, where x is a real or complex input vector, M the

transform matrix, and y the result. Examples of transforms include the discrete Fourier transform (DFT), multi-dimensional DFTs (MDDFT), the Walsh-Hadamard transform (WHT), and discrete Wavelet transforms (DWT) like the Haar wavelet. For example, for an input vector $x \in \mathbb{C}^n$, the DFT is defined by the matrix

$$DFT_n = [\omega_n^{k\ell}]_{0 \le k, \ell < n}, \quad \omega_n = \exp(-2\pi i/n).$$

Algorithms for transforms can be written using the Kronecker product formalism [2,3,5] in the form of structured sparse matrix factorizations. In the following, we use I_n to denote an $n \times n$ identity matrix, and

$$A \otimes B = [a_{k\ell}B], \ A = [a_{k\ell}]$$

for the tensor product of matrices. Further we introduce the stride permutation matrix defined, for m|n, by

$$\mathbf{L}_m^n: \ jk + i \mapsto im + j, \quad 0 \le i < k, \ 0 \le j < m.$$

Equations (1)-(6) show examples of recursive transform algorithms, written in the form of rules:

$$DFT_{mn} \to \left(DFT_m \otimes I_n \right) D_{m,n} \left(I_m \otimes DFT_n \right) L_m^{nm}$$
(1)

$$DFT_n \to X_n RDFT_n$$
 (2)

$$WHT_{mn} \to WHT_m \otimes WHT_n \tag{3}$$

$$\mathrm{MDDFT}_{n_1 \times \dots \times n_k} \to \mathrm{MDDFT}_{n_1 \times \dots \times n_r} \otimes \mathrm{MDDFT}_{n_{r+1} \times \dots \times n_k}$$
(4)

$$MDDFT_n \to DFT_n$$
 (5)

$$\operatorname{Haar}_{n} \to \operatorname{L}_{2}^{n} \left(\operatorname{I}_{n/2} \otimes \operatorname{DFT}_{2} \right) \tag{6}$$

In (1), $D_{m,n}$ is a complex diagonal matrix [2]. In (2), RDFT is the real version of the DFT (i.e., for real input) and X_n is an X-shaped matrix containing only the entries $0, \pm 1, \pm i$ [2]. The WHT is a real matrix and exists only for twopower size. WHT₂ = DFT₂ together with (3) defines the transform. In (4), the transform takes as input a $n_1 \times \cdots \times n_k$ array, stored linearized in a vector.

Spiral. Recursive application of rules like (1)-(6) yields many different algorithms for a given transform. Spiral [5] uses this fact to search for the fastest one on a given platform. A user-specified transform (like DFT₂₅₆) is expanded by Spiral using rules into a formula, which is then translated into a C program by a special formula compiler. The runtime of the program is measured and fed into a search module, which triggers, in a feedback loop, the generation of a modified formula based on a search strategy. Upon termination, Spiral outputs the fastest program found.

In this paper, we explain a crucial module in Spiral: A rewriting system that manipulates formulas to enable their direct compilation into SIMD vector code, which obviates the need for compiler vectorization.

Complex arithmetic. To describe complex transforms in terms of real arithmetic, we represent complex data vectors as real vectors using the interleaved complex format (alternating real and imaginary parts of the complex entries). Since the complex multiplication (u + iv)(y + iz) is equivalent to the real multiplication $\begin{bmatrix} u & -v \\ v & u \end{bmatrix} \begin{bmatrix} y \\ z \end{bmatrix}$, we can write the complex matrix-vector multiplication $Mx \in \mathbb{C}^n$ as $\overline{M}x' \in \mathbb{R}^{2n}$, where we define \overline{M} by replacing every entry u + iv as $\begin{bmatrix} u & -v \\ v & u \end{bmatrix}$, and x' is x in the interleaved complex format.

3 Vectorization through Rewriting

Our goal is to take formulas obtained by the recursive application of rules like (1)-(6) and automatically manipulate them into a form that enables a direct mapping into SIMD vector code. Further, we also want to explore different vectorizations for the same formula. The solution is a suitably designed rewriting system that implements our previous ideas for formula-based vectorization in [1, 8].

Formula vectorization: The basic idea. The central formula construct that can be implemented on all ν -way short vector extensions is

$$A \otimes I_{\nu},$$
 (7)

where A is an arbitrary real matrix. Vector code is obtained by generating scalar code for A (i.e., for $x \mapsto Ax$) and replacing all scalar operations by their respective ν -way vector operations. For example, c=a+b is replaced by c=vadd(a,b).

Of course, most formulas do not match (7). In these cases we manipulate the formula using rewriting rules to consist of components that either match (7) or are among a small set of base cases. It turns out that for a large class of formulas the only base cases needed are

$$L_{2}^{2\nu}, L_{\nu}^{2\nu}, L_{\nu}^{\nu^{2}}, (I_{n/\nu} \otimes L_{2}^{2\nu})\overline{D_{n}}(I_{n/\nu} \otimes L_{\nu}^{2\nu}),$$
 (8)

where D_n is any complex diagonal matrix. For $\nu = 2$ we also need the additional base case

$$\overline{[1,i]} = \begin{bmatrix} 1 & 0 & 0 & -1 \\ 0 & 1 & 1 & 0 \end{bmatrix}.$$
(9)

We assume that vectorized implementations of (8) are available. Note that $I_m \otimes L_2^{2\nu}$ converts a real vector $x' \in \mathbb{R}^{2m\nu}$ (which originates from a complex vector $x \in \mathbb{C}^{m\nu}$) from interleaved complex format into a block-interleaved complex format with block size ν . Analogously, $I_m \otimes L_{\nu}^{2\nu}$ converts back from block-interleaved into the interleaved complex format.

Definition 1. We call a formula vectorized if it is either of the form (7) or one of the forms in (8) and (9), or of the form

$$\mathbf{I}_m \otimes A \text{ or } AB, \tag{10}$$

where A and B are vectorized.

Formula manipulation. We vectorize formulas through formula manipulation using well-known mathematical identities such as (we assume that A is $n \times n$ and B and C are $m \times m$)

$$\mathbf{I}_{mn} = \mathbf{I}_m \otimes \mathbf{I}_n \tag{11}$$

$$\mathbf{I}_m \otimes A = \mathbf{L}_m^{mn} (A \otimes \mathbf{I}_m) \, \mathbf{L}_n^{mn} \tag{12}$$

$$\mathbf{L}_{n}^{kmn} = (\mathbf{L}_{n}^{kn} \otimes \mathbf{I}_{m})(\mathbf{I}_{k} \otimes \mathbf{L}_{n}^{mn})$$
(13)

$$L_{km}^{kmn} = (I_k \otimes L_m^{mn})(I_k \otimes L_n)$$

$$(15)$$

$$L_{km}^{kmn} = (I_k \otimes L_m^{mn})(L_k^{kn} \otimes I_m)$$

$$(14)$$

$$(15)$$

$$A \otimes (BC) = (A \otimes B)(A \otimes C) \tag{15}$$

$$A \otimes B = (A \otimes \mathbf{I}_m)(\mathbf{I}_n \otimes B) = (\mathbf{I}_n \otimes B)(A \otimes \mathbf{I}_m)$$
(16)

As a small example, we assume A is a real $n \times n$ matrix, and vectorize

$$I_m \otimes A$$
 (17)

for a ν -way vector instruction set.

We first apply (11) to obtain

 $\mathbf{I}_{m/\nu} \otimes \mathbf{I}_{\nu} \otimes A$

and then apply (12) to $I_{\nu} \otimes A$ to get

$$\mathbf{I}_{m/\nu} \otimes \left(\mathbf{L}_{\nu}^{n\nu} (A \otimes \mathbf{I}_{\nu}) \mathbf{L}_{n}^{n\nu} \right).$$
(18)

Note that in this formula $A \otimes I_{\nu}$ is already vectorized, but the stride permutations are not. To vectorize the stride permutations, we apply (13) and (14) to get

$$\mathbf{I}_{m/\nu} \otimes \Big((\mathbf{L}_{\nu}^{n} \otimes \mathbf{I}_{\nu}) (\mathbf{I}_{n/\nu} \otimes \mathbf{L}_{\nu}^{\nu^{2}}) (A \otimes \mathbf{I}_{\nu}) (\mathbf{I}_{n/\nu} \otimes \mathbf{L}_{\nu}^{\nu^{2}}) (\mathbf{L}_{n/\nu}^{n} \otimes \mathbf{I}_{\nu}) \Big).$$
(19)

Inspection shows that this formula is vectorized in the sense of Definition 1.

3.1 Rewriting System

Our goal is to *automatically* apply formula identities like (12)–(16) to transform given formulas into vectorized formulas. Note that the order and actual parameters chosen for each of the applied identities is a nontrivial choice. Only the correct choice will lead to vectorized formulas. Thus, automatic formula manipulation requires an appropriately designed rewriting system [4]. Specifically, it is a difficult problem to identify the right objects and rules in the system to guarantee that it is confluent and converges to fully vectorized formulas, when possible.

Vector tags. We introduce a set of tags to propagate vectorization information through the formulas and to perform algebraic simplification of permutations. Note that all objects remain matrices.

We tag a formula construct A to be translated into vector code for vector length ν by

$$\underbrace{A}_{\operatorname{vec}(\nu)} = A.$$

Further, we write

$$A \otimes_{\nu} \mathbf{I}_{\nu} = A \otimes \mathbf{I}_{\nu}$$

to stipulate that the tensor product is to be mapped into vector code as explained in Section 3 Using these tags, the base cases in (8) are expressed by

$$\underbrace{\mathbf{L}_{2}^{2\nu}}_{\mathrm{vec}(\nu)}, \ \underbrace{\mathbf{L}_{\nu}^{2\nu}}_{\mathrm{vec}(\nu)}, \ \underbrace{\mathbf{L}_{\nu}^{2\nu}}_{\mathrm{vec}(\nu)}, \ \overline{\left(\begin{array}{c}D_{n}\\ \mathbf{vec}(\nu)\end{array}\right)^{\nu}}, \ \overline{\left(1,i\right]} \, .$$

In addition we need variants of the operator $\overline{(.)}$ to handle the vectorization of complex formulas (A is assumed to be $n \times n$)

$$\overleftarrow{A}^{\nu} = \overline{A} \tag{20}$$

$$\overrightarrow{A}^{\nu} = (1 - 1)^{2(\nu)} \overline{A} \tag{21}$$

$$A = (\mathbf{I}_{n/\nu} \otimes \mathbf{L}_2^{\nu})A \tag{21}$$

$$\overline{A} = \overline{A}(I_{n/\nu} \otimes L_{\nu}^{2\nu}) \tag{22}$$

$$\overline{A}^{\nu} = (\mathbf{I}_{n/\nu} \otimes \mathbf{L}_2^{2\nu}) \overline{A} (\mathbf{I}_{n/\nu} \otimes \mathbf{L}_{\nu}^{2\nu}).$$
⁽²³⁾

(20)–(23) are the four variants of \overline{A} that have either interleaved or block-interleaved input and output format. The format conversions introduce the building blocks $L_2^{2\nu}$ and $L_{\nu}^{2\nu}$ defined in (8). A key idea in our rewriting system is to minimize these format conversions by applying the identity

$$L_2^{2\nu} L_{\nu}^{2\nu} = I_{2\nu}$$

To facilitate this simplification we introduce (20)–(23) as objects into our rewriting system. Rules (31)–(44) operate on the constructs (20)–(23) and encode the knowledge where to introduce $L_2^{2\nu}$ and $L_{\nu}^{2\nu}$ to minimize format conversion overhead.

Table 1. Stride permutation rules.

$$\underbrace{\mathbf{L}_{n}^{n\nu}}_{\operatorname{vec}(\nu)} \to \left(\mathbf{I}_{n/\nu} \otimes \underbrace{\mathbf{L}_{\nu}^{\nu^{2}}}_{\operatorname{vec}(\nu)}\right) \left(\mathbf{L}_{n/\nu}^{n} \otimes_{\nu} \mathbf{I}_{\nu}\right)$$
(24)

$$\underbrace{\mathbf{L}_{\nu}^{n\nu}}_{\operatorname{vec}(\nu)} \to \left(\mathbf{L}_{\nu}^{n} \otimes_{\nu} \mathbf{I}_{\nu}\right) \left(\mathbf{I}_{n/\nu} \otimes \underbrace{\mathbf{L}_{\nu}^{\nu^{2}}}_{\operatorname{vec}(\nu)}\right) \tag{25}$$

$$\underbrace{\mathbf{L}_{m}^{mn}}_{\operatorname{vec}(\nu)} \to \left(\mathbf{L}_{m}^{mn/\nu} \otimes_{\nu} \mathbf{I}_{\nu}\right) \left(\mathbf{I}_{mn/\nu^{2}} \otimes \underbrace{\mathbf{L}_{\nu}^{\nu^{2}}}_{\operatorname{vec}(\nu)}\right) \left(\left(\mathbf{I}_{n/\nu} \otimes \mathbf{L}_{m/\nu}^{m}\right) \otimes_{\nu} \mathbf{I}_{\nu}\right)$$
(26)

Rules. The goal is to vectorize a given formula. In our rewriting system, this is done by tagging the formula with $vec(\nu)$ and apply rules that vectorize the formula, i.e., rewrite it such that the only components tagged with $vec(\nu)$ are vectorizable base cases. Rules are applied by matching the left side of a rule

Table 2. Tensor product rules. A is an $n \times n$ matrix.

$$\left(\underbrace{A \otimes \mathrm{I}_m}_{\mathrm{vec}(\nu)}\right) \to \left(A \otimes \mathrm{I}_{m/\nu}\right) \otimes_{\nu} \mathrm{I}_{\nu} \tag{27}$$

$$\left(\underbrace{\mathbf{I}_{m}\otimes A}_{\operatorname{vec}(\nu)}\right) \to \begin{cases} \mathbf{I}_{m/\nu}\otimes\underbrace{(\mathbf{I}_{\nu}\otimes A)}_{\operatorname{vec}(\nu)}\\ \underbrace{\mathbf{L}_{m}^{mn}}_{\operatorname{vec}(\nu)}\underbrace{(\underline{A}\otimes\mathbf{I}_{m})}_{\operatorname{vec}(\nu)}\underbrace{\mathbf{L}_{n}^{mn}}_{\operatorname{vec}(\nu)} \end{cases}$$
(28)

$$\underbrace{\left(\mathbf{I}_{m}\otimes A\right)\mathbf{L}_{m}^{mn}}_{\operatorname{vec}(\nu)} \to \begin{cases} \underbrace{\mathbf{L}_{m}^{mn}}_{\operatorname{vec}(\nu)} \left(\underbrace{A\otimes\mathbf{I}_{m}}{\operatorname{vec}(\nu)}\right) \\ \left(\mathbf{I}_{m/\nu}\otimes\underbrace{\mathbf{L}_{\nu}^{n\nu}}_{\operatorname{vec}(\nu)} \left(A\otimes_{\nu}\mathbf{I}_{\nu}\right)\right) \left(\mathbf{L}_{m/\nu}^{mn/\nu}\otimes_{\nu}\mathbf{I}_{\nu}\right) \end{cases}$$
(29)

$$\underbrace{\left(\mathbf{I}_{k}\otimes\left(\mathbf{I}_{m}\otimes A^{n\times n}\right)\mathbf{L}_{m}^{mn}\right)\mathbf{L}_{k}^{kmn}}_{\operatorname{vec}(\nu)} \rightarrow \left(\underbrace{\mathbf{L}_{k}^{km}\otimes\mathbf{I}_{n}}_{\operatorname{vec}(\nu)}\right)\left(\mathbf{I}_{m}\otimes\underbrace{\left(\mathbf{I}_{k}\otimes A^{n\times n}\right)\mathbf{L}_{k}^{kn}}_{\operatorname{vec}(\nu)}\right)\left(\underbrace{\mathbf{L}_{m}^{mn}\otimes\mathbf{I}_{k}}_{\operatorname{vec}(\nu)}\right)(30)$$

Table 3. Bar operator rules (left: recursive; right: base cases). A is an $n \times n$ matrix.

 $\overrightarrow{AB}^{\nu}$

$$\underbrace{(\overline{A})}_{\operatorname{vec}(\nu)} \xrightarrow{} \underbrace{(\overline{A})}_{\operatorname{vec}(\nu)}^{\nu} (31) \qquad \qquad \overleftarrow{A \otimes_{\nu} \operatorname{I}_{\nu}}^{\nu} \rightarrow (\operatorname{I}_{n/\nu} \otimes \underbrace{\operatorname{L}_{\nu}^{2\nu}}_{\operatorname{vec}(\nu)}) (\overline{A} \otimes_{\nu} \operatorname{I}_{\nu}) \qquad (40)$$

$$\begin{array}{cccc}
AB & \to A & B & (32) \\
\overleftarrow{AB}^{\nu} & \to \overleftarrow{A}^{\nu} \overline{B}^{\nu} & (33) \\
\end{array} \qquad \overline{A \otimes_{\nu} I_{\nu}}^{\nu} \to \overline{A} \otimes_{\nu} I_{\nu} \\
\end{array} \tag{41}$$

$$\rightarrow \overline{A}^{\nu} \overline{B}^{\nu} \quad (34) \qquad \qquad \overline{A \otimes_{\nu} I_{\nu}}^{\nu} \rightarrow (\overline{A} \otimes_{\nu} I_{\nu})(I_{n/\nu} \otimes \underbrace{L_{2}^{2\nu}}_{\operatorname{Vec}(\nu)})$$
(42)

$$\begin{array}{ccc}
\overline{AB}^{\nu} \to \overline{A}^{\nu} \overline{B}^{\nu} & (35) & & & & \\
\overbrace{I_m \otimes A}^{\nu} \to I_m \otimes \overleftarrow{A}^{\nu} & (36) & & & & \overbrace{L_m^{mn}}^{mn} \to L_m^{mn} \otimes I_2 & & (43)
\end{array}$$

$$\begin{array}{ll}
\overleftarrow{\mathrm{I}_{m}\otimes A}^{\nu} \to \mathrm{I}_{m}\otimes\overleftarrow{A}^{\nu} & (37) \\
\overline{\mathrm{I}_{m}\otimes A}^{\nu} \to \mathrm{I}_{m}\otimes\overline{A}^{\nu} & (38) \\
\overline{\mathrm{I}_{m}\otimes A}^{\nu} \to \mathrm{I}_{m}\otimes\overrightarrow{A}^{\nu} & (38)
\end{array} \qquad \overbrace{\mathrm{Vec}(\nu)}^{(\nu)}^{\nu} \to (\mathrm{L}_{\nu}^{2\nu}\otimes_{\nu}\mathrm{I}_{\nu})(\mathrm{I}_{2}\otimes\underbrace{\mathrm{L}_{\nu}^{\nu}}^{2})(\mathrm{L}_{2}^{2\nu}\otimes_{\nu}\mathrm{I}_{\nu}) (44)$$

against a given expression, extracting the parameters defined in the left side, and replacing it with one of the choices in the right side parameterized by the extracted parameters. Most of our rewriting rules are shown in Tables 1–3.

The important difference between identities like (11)–(16) and rules like (24)–(44) is that the rules encode the decisions *how* to apply the identities, i.e., fix the choice of parameters. For instance, both identities (13) and (14) can be applied to $L_n^{n\nu}$ for composite *n* and ta wo-power ν ; however, only (14) with the specific choice $k = m/\nu$, $m = \nu$, and $n = \nu$ leads to a vectorized result:

$$\mathbf{L}_{n}^{n\nu} = (\mathbf{I}_{n/\nu} \otimes \mathbf{L}_{\nu}^{\nu^{2}})(\mathbf{L}_{n/\nu}^{n} \otimes \mathbf{I}_{\nu}).$$

This knowledge is encoded in rule (24) which chooses the right parameters.

Simple example. We return to our previous example (17) and explain how it is handled by our rewriting system. We start with the tagged formula

$$\underbrace{\mathbf{I}_m \otimes A}_{\operatorname{vec}(\nu)},$$

which means " $I_m \otimes A$ is to be vectorized." The system can only apply one of the alternatives of rule (28). Suppose it chooses the first alternative, which yields

$$\mathbf{I}_{m/\nu} \otimes \underbrace{(\mathbf{I}_{\nu} \otimes A)}_{\mathrm{vec}(\nu)}$$

and then applies the second alternative of (28) to $(I_{\nu} \otimes A)$, which leads to

$$\mathbf{I}_{m/\nu} \otimes \Big(\underbrace{\mathbf{L}_{\nu}^{n\nu}}_{\operatorname{vec}(\nu)} (A \otimes_{\nu} \mathbf{I}_{\nu}) \underbrace{\mathbf{L}_{n}^{n\nu}}_{\operatorname{vec}(\nu)} \Big).$$

Next, only rules (24) and (25) match, which yields

$$\mathbf{I}_{m/\nu} \otimes \Big((\mathbf{L}_{\nu}^{n} \otimes_{\nu} \mathbf{I}_{\nu}) (\mathbf{I}_{n/\nu} \otimes \underbrace{\mathbf{L}_{\nu}^{\nu^{2}}}_{\operatorname{vec}(\nu)}) (A \otimes_{\nu} \mathbf{I}_{\nu}) (\mathbf{I}_{n/\nu} \otimes \underbrace{\mathbf{L}_{\nu}^{\nu^{2}}}_{\operatorname{vec}(\nu)}) (\mathbf{L}_{n/\nu}^{n} \otimes_{\nu} \mathbf{I}_{\nu}) \Big),$$

which is the properly tagged version of the vectorized formula (19).

Example: DFT. We now show how our rewriting system vectorizes DFT_{mn} with $\nu^2 \mid mn$. The vectorization process has to overcome three crucial problems for an arbitrary two-power ν : 1) handle the interleaved complex format, 2) vectorize the stride permutation, and 3) vectorize the complex diagonal matrix. Our example shows how to cope with these problems and how to get the short-vector FFT algorithm [8].

The DFT is a complex transform, but vector instructions operate on real vectors. Thus, we have to start with $\overline{\text{DFT}}_{mn}$, tagged for vectorization. First the system commutes the vector tag using (31) and the $(.)^{\nu}$ operator and breaks the product using (32)–(39):

$$\underbrace{(\overline{\mathrm{DFT}_{mn}})}_{\mathrm{vec}(\nu)} \to \underbrace{\left((\overline{\mathrm{DFT}_m \otimes \mathrm{I}_n}) D_{m,n}(\mathrm{I}_m \otimes \mathrm{DFT}_n) \mathrm{L}_m^{mn}\right)}_{\mathrm{vec}(\nu)} \\ \to \underbrace{\left((\overline{\mathrm{DFT}_m \otimes \mathrm{I}_n}) D_{m,n}(\mathrm{I}_m \otimes \mathrm{DFT}_n) \mathrm{L}_m^{mn}\right)}_{\mathrm{vec}(\nu)} \\ \to \underbrace{\left((\overline{\mathrm{DFT}_m \otimes \mathrm{I}_n}) D_{m,n}}_{\mathrm{vec}(\nu)} \underbrace{(\mathrm{I}_m \otimes \mathrm{DFT}_n) \mathrm{L}_m^{mn}}_{\mathrm{vec}(\nu)}\right)}_{\mathrm{vec}(\nu)} \\ \to \underbrace{\left((\overline{\mathrm{DFT}_m \otimes \mathrm{I}_n}) D_{m,n}}_{\mathrm{vec}(\nu)} \underbrace{(\mathrm{I}_m \otimes \mathrm{DFT}_n) \mathrm{L}_m^{mn}}_{\mathrm{vec}(\nu)}\right)}_{\mathrm{vec}(\nu)}$$

We now continue with the three factors separately. The system applies (27) and (40) to the first factor

$$\underbrace{\left(\underbrace{\mathrm{DFT}_{m}\otimes\mathrm{I}_{n}}_{\mathrm{vec}(\nu)}\right)^{\nu}}_{\mathrm{vec}(\nu)} \to \underbrace{\left(\mathrm{DFT}_{m}\otimes\mathrm{I}_{n/\nu}\right)\otimes_{\nu}\mathrm{I}_{\nu}}_{\mathrm{vec}(\nu)}^{\nu}}_{\mathrm{vec}(\nu)} \left(\overline{\mathrm{DFT}_{m}\otimes\mathrm{I}_{n/\nu}}\otimes_{\nu}\mathrm{I}_{\nu}\right)$$

which is vectorized. The second factor is already vectorized. For the third factor, suppose the system chooses the second alternative of (30) and then breaks and propagates $\vec{(.)}^{\nu}$ using (32)–(39):

$$\underbrace{(\overline{\mathbf{I}_{m} \otimes \mathrm{DFT}_{n}}) \mathbf{L}_{m}^{mn}}_{\operatorname{vec}(\nu)} \to \overline{(\mathbf{I}_{m/\nu} \otimes \underbrace{\mathbf{L}_{\nu}^{n\nu}}_{\operatorname{vec}(\nu)} (\mathrm{DFT}_{n} \otimes_{\nu} \mathbf{I}_{\nu})) (\mathbf{L}_{m}^{mn} \otimes_{\nu} \mathbf{I}_{\nu})}^{\nu}}_{\to (\mathbf{I}_{m/\nu} \otimes \underbrace{\overline{\mathbf{L}}_{\nu}^{n\nu}}_{\operatorname{vec}(\nu)} (\mathrm{DFT}_{n} \otimes_{\nu} \mathbf{I}_{\nu})^{\nu}) (\overline{\mathbf{L}_{m}^{mn} \otimes_{\nu} \mathbf{I}_{\nu})}^{\nu}}_{\to (\mathbf{I}_{m/\nu} \otimes \underbrace{\overline{\mathbf{L}}_{\nu}^{n\nu}}_{\operatorname{vec}(\nu)} (\overline{\mathrm{DFT}_{n} \otimes_{\nu} \mathbf{I}_{\nu})}^{\nu}) (\overline{\mathbf{L}_{m}^{mn} \otimes_{\nu} \mathbf{I}_{\nu})}^{\nu}}$$

We now continue with the factors of the tensor product. One difficult part of the vectorization is the interplay of $\overline{(.)}$ and the stride permutation $L_{\nu}^{n\nu}$. First, rule (25) factors the stride permutation and then rules (32)–(39) handle $\overline{(.)}^{\nu}$. Finally rules (40)–(44) encode the rather involved manipulation required to completely vectorize:

$$\begin{split} \overline{\underbrace{\mathbf{L}_{\nu}^{n\nu}}_{\operatorname{vec}(\nu)}}^{\nu} &\to \overline{(\underbrace{\mathbf{L}_{\nu}^{n} \otimes \mathbf{I}_{\nu}}_{\operatorname{vec}(\nu)})(\mathbf{I}_{n/\nu} \otimes \underbrace{\mathbf{L}_{\nu}^{\nu^{2}}}_{\operatorname{vec}(\nu)})}^{\nu} \\ &\to \overline{(\mathbf{L}_{\nu}^{n} \otimes_{\nu} \mathbf{I}_{\nu})}^{\nu} \Big(\mathbf{I}_{n/\nu} \otimes \overline{(\underbrace{\mathbf{L}_{\nu}^{\nu^{2}}}_{\operatorname{vec}(\nu)})}^{\nu} \Big) \\ &\to \overline{(\overline{\mathbf{L}_{\nu}^{n}} \otimes_{\nu} \mathbf{I}_{\nu})} \Big(\mathbf{I}_{n/\nu} \otimes (\underline{\mathbf{L}_{\nu}^{2\nu} \otimes_{\nu} \mathbf{I}_{\nu})(\mathbf{I}_{2} \otimes \underbrace{\mathbf{L}_{\nu}^{\nu^{2}}}_{\operatorname{vec}(\nu)})(\mathbf{L}_{2}^{2\nu} \otimes_{\nu} \mathbf{I}_{\nu}) \Big) \end{split}$$

The vectorization of the remaining constructs is straight-forward using rules (40)-(44):

$$\overline{(\mathrm{DFT}_n \otimes_\nu \mathrm{I}_\nu)}^\nu \to \left(\overline{\mathrm{DFT}_n} \otimes_\nu \mathrm{I}_\nu\right)$$

and

$$\begin{split} \overrightarrow{(\mathbf{L}_m^{mn} \otimes_{\nu} \mathbf{I}_{\nu})}^{\nu} &\to (\overline{\mathbf{L}_m^{mn}} \otimes_{\nu} \mathbf{I}_{\nu}) \big(\mathbf{I}_{mn/\nu} \otimes \underbrace{\mathbf{L}_2^{2\nu}}_{\operatorname{vec}(\nu)} \big) \\ &\to \big((\mathbf{L}_m^{mn} \otimes \mathbf{I}_2) \otimes_{\nu} \mathbf{I}_{\nu} \big) \big(\mathbf{I}_{mn/\nu} \otimes \underbrace{\mathbf{L}_2^{2\nu}}_{\operatorname{vec}(\nu)} \big). \end{split}$$

Collecting the vectorized formulas yields a completely vectorized FFT

$$\begin{split} &(\mathbf{I}_{mn/\nu} \otimes \underbrace{\mathbf{L}_{\nu}^{2\nu}}_{\operatorname{vec}(\nu)}) (\overline{\mathrm{DFT}_{m} \otimes \mathbf{I}_{n/\nu}} \otimes_{\nu} \mathbf{I}_{\nu}) (\underbrace{\overline{D}_{m,n}}_{\operatorname{vec}(\nu)})^{\nu} \\ &\left(\mathbf{I}_{m/\nu} \otimes (\overline{\mathbf{L}_{\nu}^{n}} \otimes_{\nu} \mathbf{I}_{\nu}) (\mathbf{I}_{n/\nu} \otimes (\mathbf{L}_{\nu}^{2\nu} \otimes_{\nu} \mathbf{I}_{\nu}) (\mathbf{I}_{2} \otimes \underbrace{\mathbf{L}_{\nu}^{\nu^{2}}}_{\operatorname{vec}(\nu)}) (\mathbf{L}_{2}^{2\nu} \otimes_{\nu} \mathbf{I}_{\nu})) (\overline{\mathrm{DFT}_{n}} \otimes_{\nu} \mathbf{I}_{\nu}) \right) \\ &\left((\mathbf{L}_{m}^{mn} \otimes \mathbf{I}_{2}) \otimes_{\nu} \mathbf{I}_{\nu}\right) (\mathbf{I}_{mn/\nu} \otimes \underbrace{\mathbf{L}_{2}^{2\nu}}_{\operatorname{vec}(\nu)}). \end{split}$$

Inspection shows that this formula is indeed vectorized in the sense of Definition 1.

Note that there are degrees of freedom in applying our rule set, which thus yield different vectorizations. The search in the Spiral system will select the best for the given platform.

4 Experimental Results

We incorporated our rewriting system into the Spiral code generator to automatically generate vector code and search over alternative algorithms or formulas. We show runtime benchmarks on a 3 GHz Intel Pentium 4 running Windows XP and a 3.6 GHz Intel Pentium 4 running Linux kernel 2.6. We used the Intel C++ compiler 9.0 with options "/QxKW /O3 /G7 /Qc99 /Qrestrict" for vector code and "/O3 /G7 /Qc99 /Qrestrict" for scalar x86 and x87 code. These options turned out to produce the fastest code. DFT_n performance is measured in pseudo Mflop/s for floating-point code and in pseudo Mfpop/s for fixed-point code, both computed as $5n \log_2 n/(\text{runtime [ms]})$. The Haar_n wavelet performance is measured in Mfpop/s = 2N/(runtime [ms]). For all performance results higher is better. We compare our generated code with the Intel MKL 8.0 (DFTI functions) and IPP 5.0 library and with FFTW 3.1 [6] for both two-powers and multiples of ν .

2-way double-precision. Figure 1 evaluates our approach for two-way vectorization. We compare two-power DFTs of sizes $2^7 \le n \le 2^{14}$: 1) Spiral generated scalar x87 code; 2) Spiral generated SSE2 code; 3) FFTW 3.1 with enabled SSE2 support; and 4) Intel IPP 5.0 using SSE2. Spiral and FFTW achieve similar performance with FFTW being slightly faster, and IPP is between 5% and 15% faster than both. For Spiral generated code, SSE2 vectorization provides around 50% speed-up over scalar code.

4-way single-precision. Figure 2 compares DFT code for two-power sizes $2^4 \leq n \leq 2^{12}$: 1) Spiral generated scalar x87 code; 2) Spiral generated scalar x87 code vectorized by Intel's compiler (option "/QxKW"); 3) Spiral generated 4-way SSE code; 4) FFTW 3.1 with enabled SSE support; and 5) Intel IPP 5.0 using SSE. Spiral generated SSE code is up to 3 times faster as Spiral generated scalar x87 code. Using the Intel C++ compiler to vectorize code leads only to around 50% speed-up. For $16 \leq n \leq 128$, Spiral generated SSE code is clearly the



Fig. 1. Performance of DFT_n with $n = 2^k$, implemented in double-precision on a 3.6 GHz Pentium 4.

fastest. For n=256 both FFTW and IPP are slightly faster as Spiral generated SSE code. For $512 \le n \le 2048$, Spiral generated SSE code is within 10% of IPP.

8-way 16-bit fixed-point. Figure 3 compares two-power DFT fixed-point code. It shows 1) Spiral generated SSE2 code (8-way, 16-bit), 2) scalar 16-bit x86 code, and 3) Intel IPP 5.0 (16-bit). Spiral's SSE2 vectorization consistently provides speed-up of 5 times over scalar x86 code. Spiral generated SSE2 code is 2 to 2.5 times faster as the IPP and 5 to 6 times faster than Spiral generated scalar code. Figure 4 shows that for DFTs of size $n = 64 \times 2^k 3^{\ell} 5^m$ SSE2 code generated by Spiral maintains the speed-up of 5 times over scalar x86 code. IPP does only provide two-power FFTs for 16-bit fixed-point. FFTW does not provide fixed-point code.

16-way 8-bit fixed-point. Figure 5 compares implementations of the Haar wavelet: 1) Spiral generated SSE2 code, 2) Spiral generated scalar 8-bit x86 code (16-way, 8-bit), and 3) the Intel IPP 5.0 (8-bit). For Spiral, SSE2 vectorization provides a speed-up of up to 6 times over scalar x86 code. Spiral generated SSE2 code is 2 to 2.5 times faster than the IPP. FFTW does not implement Haar wavelets.

The lack of precision makes large size DFT implementations unpractical in this case.



Fig. 2. Performance of DFT_n with $n = 2^k$, implemented in single-precision on a 3 GHz Pentium 4.

5 Conclusion

SIMD vector instructions have a huge potential to speed up performance critical computational kernels with fine-grain parallelism. However, compiler support is limited and typically programmers have to resort to low-level C extensions or to assembly language programming to realize the potential of SIMD extensions. To overcome these problems for the domain of signal transforms, we presented a domain-specific vectorization framework for signal transform algorithms and in particular FFTs. The basic idea is to vectorize at a high mathematical level of abstraction, where more structural information is available as in the corresponding C code. The suitable tool for implementing this technique is a rule based rewriting system, which we included in Spiral to enable search in tandem with vectorization. Experiments with the DFT show the viability of the approach. We are currently exploring similar strategies for shared and distribute memory parallelization.

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Fig. 3. Performance of DFT_n with $n = 2^k$, implemented in 16-bit fixed-point on a 3.6 GHz Pentium 4.



Fig. 4. Performance of DFT_n with $n = 64 \times 2^k 3^\ell 5^m$, implemented in 16-bit fixed-point on a 3.6 GHz Pentium 4.



Fig. 5. Performance of the wavelet Haar_n with $n = 2^k$, implemented in 8-bit fixed-point on a 3.6 GHz Pentium 4.

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